

Microblaze Processor Reference Guide

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The MicroBlaze™ Processor Reference Guide provides information about the 32-bit and 64-bit soft processor, MicroBlaze, which is included in Vivado. The document is intended as a guide to the MicroBlaze hardware architecture.

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The MicroBlaze™ Processor Reference Guide provides information about the 32-bit soft processor, MicroBlaze, which is part of the Embedded Processor Development Kit (EDK). The document is intended as a guide to the MicroBlaze hardware architecture.

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12 [www.xilinx.com](#) MicroBlaze Processor Reference Guide 1-800-255-7778 EDK (v6.2) December 9, 2003 R Chapter 1: MicroBlaze Architecture Instructions All MicroBlaze instructions are 32 bits and are defined as either Type A or Type B. Type A instructions have up to two source register operands and one destination register operand.

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MicroBlaze is Xilinx's soft processor core optimized for embedded applications on Xilinx devices. The MicroBlaze processor is easy to use and delivers the flexibility to select the combination of peripherals, memory, and interfaces as needed. The MicroBlaze processor is commonly used in one of three preset

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Welcome to the MicroBlaze Processor Reference Guide. This document provides information about the 32-bit soft processor, MicroBlaze, included in the Embedded Processor Development Kit (EDK). The document is meant as a guide to the MicroBlaze hardware and software architecture.

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open and import the hardware platform, including the MicroBlaze processor. This Quick Start Guide will walk you through creating a basic MicroBlaze™ processor system using processor preset designs. Additional resources and information can be found on the reverse side to help you tailor a MicroBlaze processor system to your exact

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The Microblaze soft-core processor IP can be used to instantiate a processor within your FPGA design. This processor can be very useful for controlling and configuring hardware components. This section discusses how you can add a Microblaze processor and several useful components, including UART for standard output and DDR memory support, to your block design.

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