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"ASAP was originally developed as a stray-light-analysis tool for ground- and space-based imaging systems, as opposed to a straightforward lens-design software," says Shroder, noting that its ...

Photonics Products: Lens-design Software: Optical design benefits from interconnected software
Thus, new design methodologies producing more power-efficient and ... In our EDN, thick wires can carry up to 2 data. We used interconnected 2×2 hyperbars to build this EDN. Finally, an EDN needs thin ...

MpNoC Design: Modeling and Simulation
Mechanical event simulation (MES), the latest

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development in VPD products, combines the capabilities to replicate motion (kinematics), dynamic loading (kinetics), and flexing (stresses) of parts of an ...

How to combine kinematics, kinetics, and stress analysis

As a system is a set of interconnected components ... it ' s either a ready-to-use commercial platform or a reused (in-house or external) design The system hardware does not exist yet and has to be ...

System architecting by prospective performances analysis and architectural exploration from design and development of the physical robot to

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training the robot and “ digital twin ” deployment and testing. Developers have long seen the benefits of having a powerful simulation ...

Nvidia ' s Isaac robot simulations debut on Omniverse This is where the human brain shines, though, which has led many researchers to try to borrow some of its design principles to create new ... already mimic one aspect of the brain: its highly ...

A Nanowire Network That Mimics the Brain Could Inspire New Designs in AI

Digital twins have been discussed and implemented for several years in many applications from the initial

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product 3D design simulation and process design ... and the processes are interconnected. The ...

Essence of digital enterprise: Three types of digital twins

a professor of chemical and biological engineering at CU Boulder told Design News. Similarly, complex interconnected porous spaces are important in geological environments such as soils and within ...

Want Tiny Particles That can Move Through Tight Spots? Meet the Nanoswimmers

The two domains are interconnected using the SIMULIA Co-Simulation ... This example demonstrates

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the value of using high-fidelity simulation to assess fatigue life early in the design process. A ...

Simulation helps assess fatigue life of subsea jumpers-full

The most controversial 3D Zelda is remastered for the Nintendo Switch, with a new control system that doesn ' t use motion controls.

The Legend Of Zelda: Skyward Sword HD review – Link ' s most underrated adventure

UNH-IOL provides a neutral environment to foster interoperability, standards conformance and development for the interconnected world ... market

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faster across the development lifecycle, in design ...

Keysight ' s Multigigabit Automotive Ethernet Compliance Solutions Available at University of New Hampshire InterOperability Lab

Kevin Steinberg: We ran a simulation about three years ago in Davos where we had a number of global investment professionals talk about how allocating money to different sectors in particular may ...

Did Davos Fail to Anticipate the Global Recession?
Secretlab Titan Evo 2022 – Design and Features The Titan Evo is a brand new release ... The lumbar support itself is made of interconnected hinges, allowing it to

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flex with your back and remain just ...

Secretlab Titan Evo - Review

Chargeurs*PCC Fashion Technologies, the world ' s largest apparel interlinings manufacturer, and CLO Virtual Fashion, the world leader in 3D garment sim ...

Chargeurs*PCC Launches Digital Asset Studio in CLO-SET Connect to Innovate the 3D Digital Design Process CLO Virtual Fashion, the world leader in 3D garment simulation ... are interconnected to provide clients and users with a more consolidated experience. Their products include CLO, 3D design ...

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Keep Up with Advancements in the Field of Rail Vehicle Design A thorough understanding of the issues that affect dynamic performance, as well as more inventive methods for controlling rail vehicle dynamics, is needed to meet the demands for safer rail vehicles with higher speed and loads. Design and Simulation of Rail Vehicles examines the field of rail vehicle design, maintenance, and modification, as well as performance issues related to these types of vehicles. This text analyzes rail vehicle design issues and dynamic responses,

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describes the design and features of rail vehicles, and introduces methods that address the operational conditions of this complex system. Progresses from Basic Concepts and Terminology to Detailed Explanations and Techniques Focused on both non-powered and powered rail vehicles—freight and passenger rolling stock, locomotives, and self-powered vehicles used for public transport—this book introduces the problems involved in designing and modeling all types of rail vehicles. It explores the applications of vehicle dynamics, train operations, and track infrastructure maintenance. It introduces the fundamentals of locomotive design, multibody dynamics, and longitudinal train dynamics, and

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discusses co-simulation techniques. It also highlights recent advances in rail vehicle design, and contains applicable standards and acceptance tests from around the world. • Includes multidisciplinary simulation approaches • Contains an understanding of rail vehicle design and simulation techniques • Establishes the connection between theory and many simulation examples • Presents simple to advanced rail vehicle design and simulation methodologies Design and Simulation of Rail Vehicles serves as an introductory text for graduate or senior undergraduate students, and as a reference for practicing engineers and researchers investigating performance issues related to these types of vehicles.

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The book presents a series of articles devoted to modeling, simulation, and optimization of processes, mainly chemical. General methods for process modeling and numerical simulation are described with flowsheeting. Population balances are addressed in detail with application to crystal production; energy saving is frequently optimized, including exergy analysis. The coupling between process simulation and computational fluid dynamics is studied for air classification and bubble columns. Pressure swing adsorption, reactive distillation, and nanofiltration are explained in general and applied to particular processes. The synthesis of carbon dots is solved by

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the design of experiments method. A safety study addresses the consequences of gas explosion.

Large-scale interconnected systems have become more prominent in society due to a higher demand for sustainable development. As such, it is imperative to create effective methods and techniques to control such systems. Large-Scale Fuzzy Interconnected Control Systems Design and Analysis is an innovative source of academic research that discusses the latest approaches to control large-scale systems, and the challenges that occur when implementing them. Highlighting a critical

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range of topics such as system stability, system stabilization, and fuzzy rules, this book is an ideal publication for engineers, researchers, academics, graduate students, and practitioners interested in the design of large-scale interconnected systems.

This book constitutes the refereed proceedings of the 21st International Conference on Integrated Circuit and System Design, PATMOS 2011, held in Madrid, Spain, in September 2011. The 34 revised full papers presented were carefully reviewed and selected from numerous submissions. The paper feature emerging challenges in methodologies and tools for the design of upcoming generations of integrated circuits and

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systems and focus especially on timing, performance and power consumption as well as architectural aspects with particular emphasis on modeling, design, characterization, analysis and optimization.

In Interconnect-centric Design for Advanced SoC and NoC, we have tried to create a comprehensive understanding about on-chip interconnect characteristics, design methodologies, layered views on different abstraction levels and finally about applying the interconnect-centric design in system-on-chip design. Traditionally, on-chip communication design has been done using rather ad-hoc and informal approaches that fail to meet some of the challenges posed by next-

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generation SOC designs, such as performance and throughput, power and energy, reliability, predictability, synchronization, and management of concurrency. To address these challenges, it is critical to take a global view of the communication problem, and decompose it along lines that make it more tractable. We believe that a layered approach similar to that defined by the communication networks community should also be used for on-chip communication design. The design issues are handled on physical and circuit layer, logic and architecture layer, and from system design methodology and tools point of view. Formal communication modeling and refinement is used to bridge the communication layers, and network-centric

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modeling of multiprocessor on-chip networks and socket-based design will serve the development of platforms for SoC and NoC integration. Interconnect-centric Design for Advanced SoC and NoC is concluded by two application examples: interconnect and memory organization in SoCs for advanced set-top boxes and TV, and a case study in NoC platform design for more generic applications.

Helps you choose the right computational tools and techniques to meet your drug design goals
Computational Drug Design covers all of the major

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computational drug design techniques in use today, focusing on the process that pharmaceutical chemists employ to design a new drug molecule. The discussions of which computational tools to use and when and how to use them are all based on typical pharmaceutical industry drug design processes. Following an introduction, the book is divided into three parts: Part One, The Drug Design Process, sets forth a variety of design processes suitable for a number of different drug development scenarios and drug targets. The author demonstrates how computational techniques are typically used during the design process, helping readers choose the best computational tools to meet their goals. Part Two, Computational Tools and

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Techniques, offers a series of chapters, each one dedicated to a single computational technique. Readers discover the strengths and weaknesses of each technique. Moreover, the book tabulates comparative accuracy studies, giving readers an unbiased comparison of all the available techniques. Part Three, Related Topics, addresses new, emerging, and complementary technologies, including bioinformatics, simulations at the cellular and organ level, synthesis route prediction, proteomics, and prodrug approaches. The book's accompanying CD-ROM, a special feature, offers graphics of the molecular structures and dynamic reactions discussed in the book as well as demos from computational drug design software companies.

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Computational Drug Design is ideal for both students and professionals in drug design, helping them choose and take full advantage of the best computational tools available. Note: CD-ROM/DVD and other supplementary materials are not included as part of eBook file.

Streamlined Design Solutions Specifically for NoC To solve critical network-on-chip (NoC) architecture and design problems related to structure, performance and modularity, engineers generally rely on guidance from the abundance of literature about better-understood system-level interconnection networks. However, on-chip networks present several distinct challenges that require novel and specialized solutions not found in the

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tried-and-true system-level techniques. A Balanced Analysis of NoC Architecture As the first detailed description of the commercial Spidergon STNoC architecture, Design of Cost-Efficient Interconnect Processing Units: Spidergon STNoC examines the highly regarded, cost-cutting technology that is set to replace well-known shared bus architectures, such as STBus, for demanding multiprocessor system-on-chip (SoC) applications. Employing a balanced, well-organized structure, simple teaching methods, numerous illustrations, and easy-to-understand examples, the authors explain: how the SoC and NoC technology works why developers designed it the way they did the system-level design methodology and tools

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used to configure the Spidergon STNoC architecture differences in cost structure between NoCs and system-level networks From professionals in computer sciences, electrical engineering, and other related fields, to semiconductor vendors and investors – all readers will appreciate the encyclopedic treatment of background NoC information ranging from CMPs to the basics of interconnection networks. The text introduces innovative system-level design methodology and tools for efficient design space exploration and topology selection. It also provides a wealth of key theoretical and practical MPSoC and NoC topics, such as technological deep sub-micron effects, homogeneous and heterogeneous processor architectures, multicore

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SoC, interconnect processing units, generic NoC components, and embeddings of common communication patterns.

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